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processing signals for a smart card, said reading apparatus comprising:[having] an electronic circuit [(2)] driven to generate the sequence of control signals in a forwards and reverse direction along the same circuit path; and [wherein the reading apparatus includes] a timing means to allow a sequence of control signals to be activated in a pre-determined order for operation of the card and deactivated in a reverse order for disabling the card [and characterised in that the electronic circuit is driven to generate the sequence of control signals in a forwards and reverse direction along the same circuit path].

REMARKS

Attached is the clean version of the claims and new paragraphs as required in Section 1.121(4) (ii).

The application should now be in condition for examination, which is respectfully requested.

Respectfully Submitted

HEAD, JOHNSON & KACHIGIAN

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